

09/831532

SECTION  
CLASSIFICATION

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED *TR4* *QA* *TM*

I.P.E.

PATENT DATE

APPLICATION NO. 09/831532	CONT/PRIOR D F	CLASS 372	SUBCLASS 75	ART UNIT 2851	EXAMINER <i>Werner</i>
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APPLICANTS

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TITLE

Diode array integrated circuit

PTO-2040  
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____  <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg.    Figs. Drwg.    Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims    Print Claim for O.G.	
	(Assistant Examiner)    (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
	(Primary Examiner)    (Date)			<b>ISSUE FEE</b> Amount Due    Date Paid	
	(Legal Instruments Examiner)    (Date)			<b>ISSUE BATCH NUMBER</b>	

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